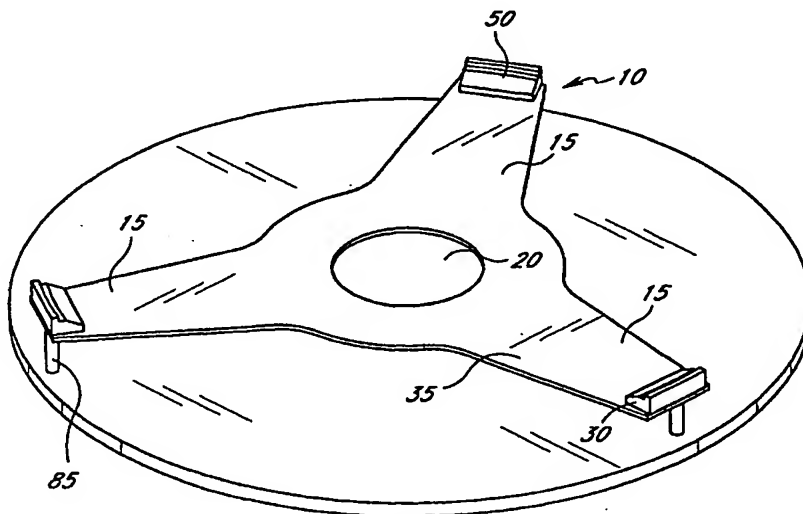




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(54) Title: WAFER CARRIER AND METHOD FOR HANDLING OF WAFERS WITH MINIMAL CONTACT



(57) Abstract

Disclosed is a carrier (10) comprising three support elements (50) connected by an underlying frame (15). The periphery of a wafer (45) rests upon the support elements (50). Also disclosed is a wafer handler (25) with a plurality of arms (100). Spacers (85) space the carrier (10) above a base plate (90) associated with a station in a wafer handling area. An arm (100) slides beneath the frame (15) and between the spacers (85), but the handler (25) does not contact the wafer (45). A method of using the handler (25) and carrier (10) is provided where the handler (55) lifts and rotates the carrier (10) with the wafer (45) through various stations (112, 125, 126 and 130) in a wafer handling area (95). A control device (33) reduces the handler speed only at critical points of the processing cycle. The handler (55) is capable of moving a plurality of carriers (10) and wafers (45) simultaneously.

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WAFER CARRIER AND METHOD FOR HANDLING OF WAFERS WITH MINIMAL CONTACT

Field of the Invention

This invention relates to an apparatus and method for facilitating the handling and transport of semiconductor wafers, and more particularly to an apparatus and method for protectively supporting semiconductor wafers in a manner
5 which permits easy transport between processing stations and between cassettes or other wafer transport/holding mechanisms with minimal risk of damage and contamination of the wafer.

Background of the Invention

During semiconductor wafer processing, the wafers frequently move through several locations such as: a cassette, a load lock station where the atmosphere around the wafer is purged and the wafer may be etched, a pre-
10 processing station where the wafer may be measured or cleaned, a reactor, and a post-processing station where the wafer is cooled, and the thickness of a deposition layer may be measured. The wafer obviously must be handled in order to move to each location; however, each time the wafer is contacted or jarred, particles may be generated, and the likelihood of backside scratching or contamination by such particles is increased. These wafers can be rendered useless for device fabrication by contamination, abrasion, or damage. Therefore, extreme care is required to support and transport wafers.

15 U.S. Patent No. 5,046,909 by Murdoch teaches clipping the wafer onto a retaining ring so that the ring is handled during processing instead of directly handling the wafer. A robotic arm moves the wafer to be engaged with the clips. However, the clips contact both the top and bottom of the wafer, and great precision is required in order to initially engage the wafer with the clips.

U.S. Patent No. 4,306,731 by Shaw, and U.S. Patent No. 4,779,877, also by Shaw, teach an apparatus
20 comprising a plate-like support provided with an aperture of a diameter larger than the wafer, and clip means for gripping the edge of the wafer. The leading edge of each clip has an arcuate portion within which the edge of the wafer is retained. However, the Shaw inventions require that the clip must first be spread before receiving the wafer to prevent edgewise abrasion. Thus, these inventions are rather complex because additional parts are needed to actuate the clip, such as a pneumatic cylinder and contact pin.

25 U.S. Patent No. 4,473,455 by Dean et al. teaches spring-mounted members disposed around the periphery of an aperture in a wafer-mounting plate. However, once the wafer is retained within the wafer-mounting plate, the wafer cannot be moved with the plate because this invention is only for use of holding the wafer during a processing step.

European Patent Application (Publication number 0 634 784 A1) by A. Tepman discloses a variable speed wafer exchange robot. However, the design is quite complicated, especially that of the wafer support assembly.

30 Consequently, a need exists for a simple wafer carrying device which is simple to use, has few parts, transportable through multiple locations, minimizes contact with the wafer, and reduces the potential for particle generation.

Summary of the Invention

The present invention is a carrier that supports a wafer during most stages of wafer handling, and a method for
35 using the carrier with a wafer handler which moves the carrier and wafer to multiple stations.

The wafer handler has multiple sensors which sense the location of the handler at different points of the processing cycle. A control device receives location information from the sensors and sends signals to a variable speed drive motor which changes the acceleration and speed of the handler based on this information. When the handler moves vertically to place or pick up the wafer carriers from the stations, the control device decreases the handler acceleration and speed to ensure a smooth transition and prevent the carriers from shifting or chattering which may cause particle generation, thereby contaminating the wafers. However, immediately after the handler picks up the carrier during the handler's motion upward or places the carrier down on a base plate, the handler accelerates and increases in speed in order to maximize throughput. The handler moves with the fastest speed when it is not transporting any wafer carriers.

An end effector removes the wafer from a cassette and places the wafer onto the carrier. The carrier has a plurality of support elements defining a wafer support plane, and a support structure or frame below the plane connecting the support elements. The periphery of the wafer rests upon the support elements. The wafer can then be moved by lifting the carrier rather than engaging the wafer.

In one example, a wafer handler arm is inserted beneath the carrier so that the handler does not contact the wafer. The wafer handler may be provided with a plurality of arms that lift and rotate a plurality of carriers with wafers from station to station. The wafers rest safely atop the carriers, so that the handling is absorbed by the carrier instead of the wafer.

After processing, an end effector removes the wafer from the carrier back to the cassette, leaving the carrier in the handling chamber for repeated use. The use of the carrier in an environment where the wafer must be repeatedly picked up and set down reduces the number of times the wafer is contacted. This reduces the amount of backside marking on the wafer. Additionally, the carrier has locating features which assist in centering the wafer to be properly positioned for processing.

Brief Description of the Drawings

- FIG. 1 is a perspective view of a wafer carrier supported by spacers extending from a base plate.
- FIG. 2 is a perspective view of an end effector carrying a wafer.
- FIG. 3 is a perspective view of an end effector placing a wafer on top of the support blocks of a carrier.
- FIG. 4 is a perspective view of a withdrawn end effector after placing a wafer on top of the support blocks of a carrier.
- FIG. 5 is a sectional view of the carrier, an outer end of a wafer handler arm, and a base plate along line 5-5 of FIG. 1.
- FIG. 6 is a top view of the wafer handling area where the wafer handler is in a neutral position.
- FIG. 7 is a top view of the wafer handling area after the wafer handler has rotated 45° counterclockwise so that the outer end of each wafer handler arm is beneath a carrier and above a base plate.
- FIG. 8 is a top view of the wafer handling area after the wafer handler has moved the wafers to the next station.
- FIG. 9 is a schematic view of the wafer handling area.
- FIG. 9A is a schematic view of the output shaft, flags, sensors, control device, and drive motor.
- FIG. 10 is a graph illustrating the speed of the wafer handler versus the z-axis position of the wafer handler.

Detailed Description of the Preferred Embodiment

Referring now to the drawings, and particularly to FIG. 1, a carrier of the invention is shown schematically and indicated generally by the number 10. In a preferred embodiment, the carrier 10 is shown with a frame or structure formed by three flat arms 15 extending outward from its center and defining a generally flat wall. The carrier 10 is preferably made of a metal such as aluminum, including anodized aluminum, or a ceramic material such as alumina, or any material which will not interfere with wafer processing. The carrier 10 may have a hole 20 in its center to decrease its weight. The arms 15 have portions 35 positioned inwardly with respect to the outer ends 30. The inner portions 35 are spaced beneath the plane to enable an end effector 40 to extend beneath the plane to facilitate the transfer of a wafer 45 between the end effector 40 and the carrier 10. In the arrangement shown in FIGS. 2-4, the end effector 40 is a flat paddle, and the space between the wafer support plane and the arm portions 35 is sufficient to enable the end effector 40 to be inserted in that space. For other types of end effectors which place the wafer upon the carrier from a different position, the space may be minimal.

The configuration of the carrier 10 may be other than the "star" embodiment shown in FIG. 1, as long as the carrier provides adequate support to the periphery of the wafer, and an underlying frame or support structure connecting the points are configured so that an end effector may transfer the wafer to and from the carrier, such as an end effector fitting into the wafer support plane and retracting after placing the wafer on the carrier.

A wafer support block 50 is connected to the outer end 30 of each carrier arm 15. The wafer support blocks 50 define a horizontal wafer support plane. The wafer support block is preferably made of quartz or a high temperature plastic. One suitable example is polybenzimidazole sold under the trademark Celazole PBI. In the embodiment shown in FIG. 5, the block 50 has a lip 55 at the top of the periphery to restrain the wafer 45 from horizontal movement, and an upper support surface 60 upon which the wafer 45 rests. The lip 55 also helps in automatically centering the wafer when it is placed on the carrier by the end effector. The upper support surface 60 may have a chamfer 65 to minimize the contact area between the wafer support block 50 and the wafer 45. This also prevents the wafer from falling in between the blocks in case the wafer is not exactly centered. The upper support surface 60 and chamfer 65 are sized such that only the portion of the wafer which will not be used, commonly known as the "exclusion zone," contacts the wafer support block 50. Each block 50 is attached by a pair of screws (not shown) which extend through the carrier arm and thread into the block. Of course, other attachment means may be employed. The block 50 has a protuberance 70 which is received through a hole 75 in the carrier arm 30. The protuberance 70 has a downwardly facing socket or recess 80 to receive a spacer 85. The spacer 85 may either be connected to the carrier 10 or extend upward from a base plate 90 in the wafer handling area 95 (see FIGS. 6-8). The spacers 85 create a gap between the bottom of the carrier 10 and the top of the base plate 90 where a wafer handler 25 is inserted to transport the carrier 10 and wafer 45 without contacting the wafer 45. The spacer and socket combinations help in precise positioning of the carriers as they are moved from stations to station by the handler. The carrier 10 need not have separate wafer support blocks 50, but may have support points of unitary construction with the carrier 10.

The carrier is useful to minimize contact with the wafer in any wafer handling operation with robotic end effectors or wafer handlers. FIGS. 6-8 illustrate an arrangement for moving the carrier in a circular pattern. The wafer handler 25 has four arms 100 extending from its center at 90° intervals. The wafer handler 25 mounted on an output shaft 11, seen in FIG. 9, can rotate in a planar direction about its center in both a clockwise and counterclockwise direction. The output shaft is rotated by a variable speed drive motor 34, schematically shown in FIG. 9A. A bellows 103 permits the movement of handler 25 in the vertical direction. Four flags 26, 27, 28, and 29 are located on an input shaft 21 connected by a suitable gear and cam mechanism (not shown) to the output shaft 11. The flags trigger two sensors: a Z-lowered sensor 31, and a Z-raised sensor 32 which sense the position of the wafer handler 25 during different points of the processing cycle (see FIGS. 9, 9A). The sensors 31, 32 send signals to a control device 33 which changes the speed and acceleration of the wafer handler 25 by way of the variable speed drive motor 34.

The thickness of the tip of each arm 100 is less than the height of the space between the bottom of the carrier 10 and the top of the base plate 90, so that the handler arm 100 may be inserted in said space without shifting the carrier 10 relative to the base plate 90. While the wafer handler 25 transports carrier 10, the central portion of the carrier 10 rests upon pins 105 extending upward from the outer edge of the handler arm 100. Preferably three pins provide adequate support, while limiting contact to minimize particle formation.

Each of the stations in the wafer handling area 95 includes a base plate, approximately the same size as the wafer to be processed, and three spacers 85. The elevation and orientation of the spacers 85 facilitate the insertion of the wafer handler arms 100 under the carriers 10. The first station 112 is below the load lock chamber 110 where the atmosphere around the wafer 45 is purged. The second station may be a pre-processing station 125 where the wafer 45 may be measured or cleaned. At the third station 126, the wafer may be lifted and transferred through a gate 120 to the processing chamber and then returned to the carrier 10 after processing. The fourth station may be a post-processing station 130 where the wafer 45 may be cooled, and the thickness of a deposition layer may be measured.

In operation, the robotic arm end effector 40 is inserted beneath the wafer 45 and places the wafer 45 onto the support blocks 50 on a carrier 10. The arms 100 of the wafer handler 25 rotate 45° counterclockwise from the neutral position shown in FIG 6 to the position shown in FIG 7. Referring to points A to B of FIG. 10, the wafer handler 25 is not carrying the carrier 10, so the wafer handler operates at a fast speed I (see FIG. 10).

As seen, an arm 100 is inserted between the spacers 85 of the first base plate 90, and between the first base plate 90 and below the carrier 10. The wafer handler arm 100 then lifts the carrier 10 with the first wafer 45 so that the carrier 10 with the first wafer 45 is resting on the pins 105 at the end of the wafer handler arm 100. Immediately before the wafer handler arm 100 lifts carrier 10, the first flag 26 triggers the first transition of the Z-lowered sensor 31, which sends a signal to the control device 33 to reduce the speed of the wafer handler 25 to a slow speed II (see FIG. 10, points B-D). Consequently, the vertical pick-up of the wafer 45 occurs very gently which minimizes particle generation of the wafer 45 by preventing shifting and chattering of the carrier 10.

As soon as the wafer handler 25 picks up the carrier 10 with the first wafer 45, the second flag 27 triggers the first transition of the Z-raised sensor 32 which sends a signal to the control device 33 to increase the speed of the wafer

handler 25 to a medium speed III. Therefore, the speed of wafer handler 25 is minimized only at the most critical points of carrier transport, thereby maximizing throughput. Wafer handler 25 then rotates 90 counterclockwise at speed III until the carrier 10 and first wafer 45 are directly above a second base plate 124 (points D-F of FIG. 10). At point F, the third flag 28 triggers the second transition of the Z-raised sensor 32, and the handler lowers the carrier 10 and first wafer 45 at slow speed II (FIG. 10, points F-H)

5 onto the second base plate 124 to facilitate a smooth wafer set-down and centers the carrier 10 so that the recesses 80 in the protuberances 70 of the wafer support blocks 50 mate with the respective spacers 85. Note that the recesses 80 are tapered to guide the spacers 85 into the desired position in the event of any slight misalignment of the carrier 10 on the handler arm 100.

10 After transferring the carrier 10 to the spacers 85 on the plate 124, the pins 105 on the end of the wafer handler arm 100 are no longer contacting the carrier 10. Now, the fourth flag 29 triggers the second transition of the Z-lowered sensor 31, and the wafer handler 25 rotates clockwise at fast speed I (FIG. 10, point H) so that the wafer handler arm 100 is no longer under the carrier 10 and the first wafer. Note that the spacers 85 must be properly oriented to permit this wafer handler arm 100 movement. With the spacers properly oriented, the rotation sequence could be reversed

15 so that the arm moves 45 clockwise, then 90 clockwise, and then 45 counterclockwise to move the carrier 10 in a clockwise direction. This wafer movement process repeats so that the wafer handler 25 lifts and rotates the carrier 10 and wafer 45 through all the stations of the wafer handling area 95.

When the carrier 10 and wafer 45 return to the first base plate 90, the end effector 40 removes the wafer 45 from the carrier 10 and returns the wafer 45 to the cassette (wafer 45 can either go to the same place in the cassette or any other slot in the cassette). While the first wafer 45 is located at the second station 125, the end effector 40 transfers a second wafer to a second carrier at the first station 110. The wafer handler 25 similarly lifts and rotates the second wafer on the second carrier through the stations of the wafer handling area 95. As shown, four carriers 10 reside with the system at all times and the wafer handler arms 100 continuously transport the carriers 10 from one position to another. Thus, four wafers may be moved through the wafer handling area 95 at one time, with one wafer at each

20

25 station.

It is possible to program different speeds for the wafer handler movements with the carrier, without the carrier, and while placing and picking up the carriers from the stations. This allows optimization of the handling time while also ensuring minimal particle generation, and gives the operator flexibility in customizing a motion profile specific to any desired application.

30 In one specific arrangement, the robotic arm end effector 40 is inserted beneath a first wafer 45 waiting to be processed from a slot in a cassette or other wafer source (not shown). The end effector 40 transfers the wafer 45 from the cassette to the support blocks 50 on a carrier 10. The carrier 10 is located on the first base plate 90 in the wafer handling area 95. A first gate valve 97 opens so that an elevator 107 raises the first base plate 90 into the load lock chamber 110, positioned above the first base plate 90, until the base plate 90 properly seats in the load lock chamber 110.

The edge of the base plate 90 forms an air tight seal with a cylindrical entry to the load lock chamber 110. While the elevator is in the raised position and the first gate valve 97 closes, the load lock chamber 110 is purged with a purge gas.

After the purging is finished inside the load lock chamber 110, the elevator 107 below the first base plate 90 lowers the first base plate 90 from the load lock chamber 110. Then, as previously discussed above, one of the arms 100
5 of the wafer handler 25 rotates 45 counterclockwise at speed I from the neutral position shown in FIG 6 to the position shown in FIG 7. The same steps as discussed above are followed to move the wafer 45 on the carrier 10 with the wafer handler 25 to the second base plate 124.

A second gate valve 120 is positioned adjacent to a third base plate 128. An elevator 117 below the third base plate 128 raises the third base plate 128 to a higher level so that a robotic arm (not shown) may retrieve the wafer 45 to
10 the processing chamber (not shown). Bellows 103 is preferably mechanically controlled while elevators 107 and 117 are preferably pneumatic devices. The gate valve 120 then closes and the wafer 45 is ready for processing.

After processing, the gate valve 120 opens and the robotic arm transfers the wafer 45 back to the carrier 10 on the third base plate 128, and the elevator 117 lowers the third base plate 128 at speed II. Then, as discussed above, the wafer handler arms 100 rotate the carrier 10 with the processed wafer 45 at speed III to the fourth base plate 130 for
15 post-processing.

Once post-processing is completed, the wafer handler arms 100 once again move carrier 10 with wafer 45, as described above, back to the first base plate 90. Here, the end effector 40 is inserted beneath the wafer 45, and lifts the wafer 45 from the carrier 10 to return the wafer 45 back to the cassette.

As can be seen, four wafers may be moved through the four stations at one time, one wafer at each station.
20 The use of the carrier 10 with the wafer 45 is not limited to transporting the wafer 45 through a wafer handling station, but may also be used in any application where a wafer must be moved. Also, the wafer handler 25 described above is not limited to rotational movement, but may move in any direction to move a carrier from one location to another.

WHAT IS CLAIMED IS:

1. An apparatus for handling semiconductor wafers comprising:
a carrier for supporting a wafer as the carrier is transferred from one location to another, said carrier having a plurality of arms having outer ends defining a wafer support plane, the outer ends being configured to engage the periphery of a wafer, said arms having interconnecting portions positioned inwardly with respect to the outer ends, and configured to enable an end effector to facilitate the transfer of the wafer between the end effector and the carrier.
2. The apparatus of Claim 1, including a plurality of spacers which connect with said carrier to space the carrier above a support surface to allow the insertion of a wafer handler beneath the carrier so as to move the wafer from one location to another without contacting the wafer.
3. The apparatus of Claim 1 wherein said arms are adapted to cooperate with the spacers which space the carrier above a support surface to permit the insertion of a wafer handler arm beneath the carrier.
4. The apparatus of Claim 1 wherein the arm outer ends are spaced to permit the insertion of the end effector beneath said wafer support plane.
5. The apparatus of Claim 1, wherein said carrier has a central hub and multiple arms extending outwardly from the hub.
6. The apparatus of Claim 1 wherein each of the carrier arm outer ends include a wafer support block to engage the wafer.
7. The apparatus of Claim 6, wherein said support block includes a lip at the top of the periphery of said support block to automatically center and restrain the wafer from horizontal movement, and an upper support surface upon which the wafer rests.
8. The apparatus of Claim 7 wherein each wafer support block has a portion which mates with a spacer for spacing the carrier above a support surface.
9. The apparatus of Claim 6 wherein each wafer support block has a protuberance which extends through a hole in the carrier arm.
10. The apparatus of Claim 9 wherein the protuberance has a recess to receive the spacer.
11. An apparatus for handling semiconductor wafers comprising:
a carrier having a plurality of wafer support elements defining a wafer support plane,
a supporting frame below the wafer support plane;
a wafer handler for moving the carrier, said handler having a plurality of arms extending outwardly from the center of said wafer handler, each arm being adapted to be positioned beneath the carrier;
a control device for controlling the speed and position of said wafer handler; and
a plurality of sensors on said wafer handler for sensing the position of said wafer handler.
12. The apparatus of Claim 11 wherein said control device controls said wafer handler at a plurality of speeds which are compatible with the speed and safety of the movement of said carrier.

13. The apparatus of Claim 11 wherein said frame includes a plurality of arms having outer ends defining said wafer support plane, said carrier having arm portions positioned inwardly with respect to the outer ends, and spaced beneath said wafer support plane sufficiently to enable an end effector to extend beneath said plane and above said arm portions to facilitate the transfer of the wafer between the end effector and the carrier.

5 14. The apparatus of Claim 11 wherein said wafer handler is capable of planar rotation about a center of said wafer handler.

15. The apparatus of Claim 11, wherein said wafer handler is capable of vertical movement.

16. The apparatus of Claim 15, wherein said control device controls the wafer handler to operate at a fast speed when said wafer handler arm is not positioned beneath the carrier, at a slow speed during vertical movement when said wafer handler arm is positioned beneath the carrier, and at a medium speed during rotational movement when said wafer handler is positioned beneath the carrier.

17. The apparatus of Claim 11, wherein said carrier has a central hub and three arms extending outwardly from the hub.

18. The apparatus of Claim 11, wherein the wafer handler has three pins located on the outer end of the upper surface of the handler arms, said pins being made of material which will not interact with the carrier to substantially produce contaminants.

19. An apparatus for handling semiconductor wafers comprising:
a carrier having three equally spaced arms radiating from a central hub,
each carrier arm adapted to be engaged by a spacer which extends upwardly from a support to space said carrier from said support,

a wafer support plane defined by the three outer portions of said arms, said plane being sufficiently spaced above said central hub to enable a robotic arm end effector to extend beneath said plane and above said arm portions to facilitate the transfer of the wafer between the end effector and the carrier, and

the outer portion of each carrier arm having a wafer support block for engaging the periphery of a wafer, said wafer support block having a unitary lip at the top of the outer periphery of said support block to restrain the wafer from horizontal movement, and an upper support surface upon which the wafer rests, the bottom surface of each wafer support block having a protuberance which mates with the spacer extending from the base plate, and the outer portion of each carrier arm having at least one hole which receives the protuberance of the wafer support block, a wafer handler for moving the carrier, a control device for controlling the speed and position said wafer handler, and a plurality of sensors on said wafer handler for sensing the position of said wafer handler.

20. The apparatus of Claim 19 wherein said handler has a plurality of arms radiating from the center of said wafer handler, said wafer handler being capable of vertical movement and planar rotation about the center of said wafer handler to enable each handler arm to move beneath a carrier, and transfer the carrier and the wafer from one location to another without contacting the wafer.

21. The apparatus of Claim 19, wherein said wafer handler operates at a fast speed when said wafer handler arm is not positioned beneath the carrier, at a slow speed during vertical movement when said wafer handler arm is positioned beneath the carrier, and at a medium speed during rotational movement when said wafer handler is positioned beneath the carrier.

5 22. A carrier for a semiconductor wafer comprising:
a support structure having a bottom with a generally flat upper surface; and
a plurality of wafer supports connected to and extending above said bottom and defining a wafer support plane generally parallel to said bottom, said bottom having a generally flat lower surface to be engaged by a wafer handler for moving the carrier, and a wafer on the carrier, from one location to another without having the handler contact the wafer.

10 23. The carrier of Claim 19 wherein said carrier has multiple radially extending arms defining said bottom.

24. A method of handling a semiconductor wafer comprising the steps of:
placing a wafer atop a carrier having a plurality of support elements defining a wafer support plane, and having a frame below said plane connecting the support elements;
inserting an end effector carrying a wafer between the plane and the frame to transfer the wafer to a carrier;
15 and
moving said wafer from a first station to a second station by moving the carrier without contacting said wafer.

25. The method of Claim 24 wherein the step of moving said carrier and wafer comprises:
inserting an arm of a wafer handler between the carrier and a support;
20 lifting said carrier from the support;
moving said wafer handler with said carrier and wafer supported by said arm to the second station;
lowering said carrier and wafer onto a plurality of spacers at said second station; and
moving said wafer handler arm without said carrier and wafer to a position out from beneath the carrier.

26. The method of Claim 24, wherein said moving step includes rotating said handler.

25 27. The method of Claim 25, wherein a plurality of wafers and carriers are lifted and rotated by wafer handler arms, to a plurality of stations, said handler arms moving beneath the carriers and avoiding contact with the spacers associated with each station that support each carrier.

28. The method of Claim 25, wherein said lifting step includes vertically lifting said wafer handler arms at a slow speed.

30 29. The method of Claim 25, wherein said moving said wafer handler with said carrier and wafer step includes rotating said wafer handler at a medium speed.

30. The method of Claim 25, wherein said lowering step includes vertically lowering said wafer handler arms at a slow speed.

35 31. The method of Claim 25, wherein said moving said wafer handler arm without said carrier and wafer step includes rotating said wafer handler at a fast speed.

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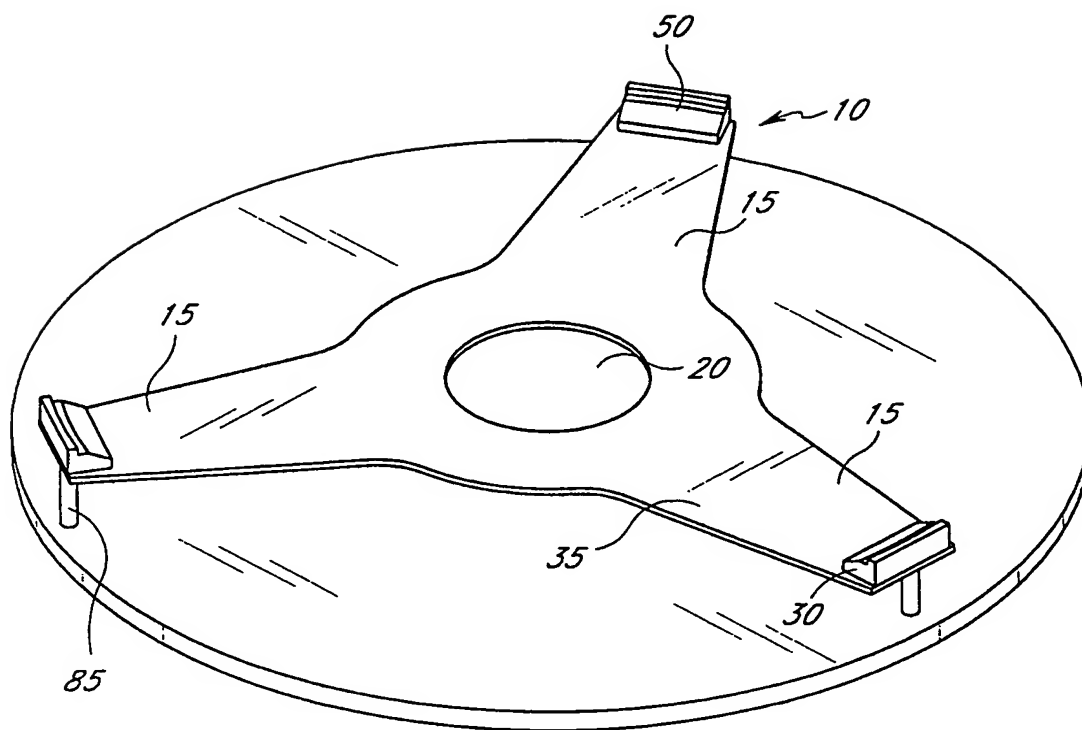
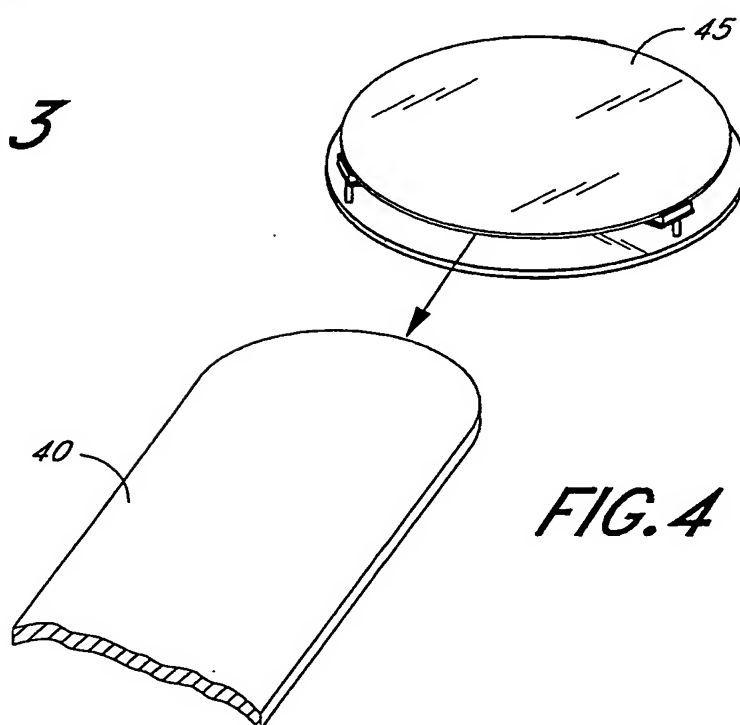
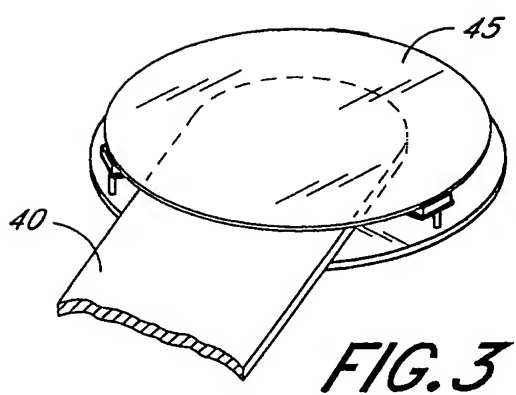
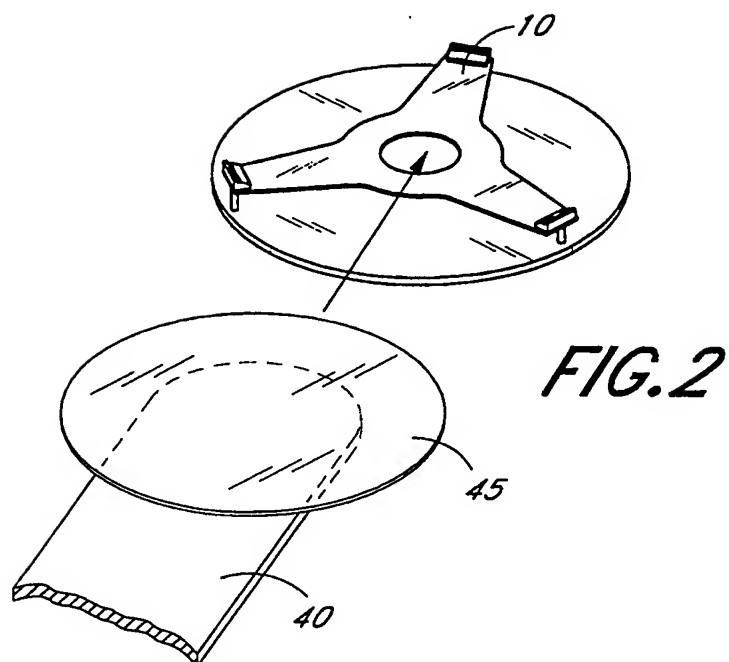


FIG. 1

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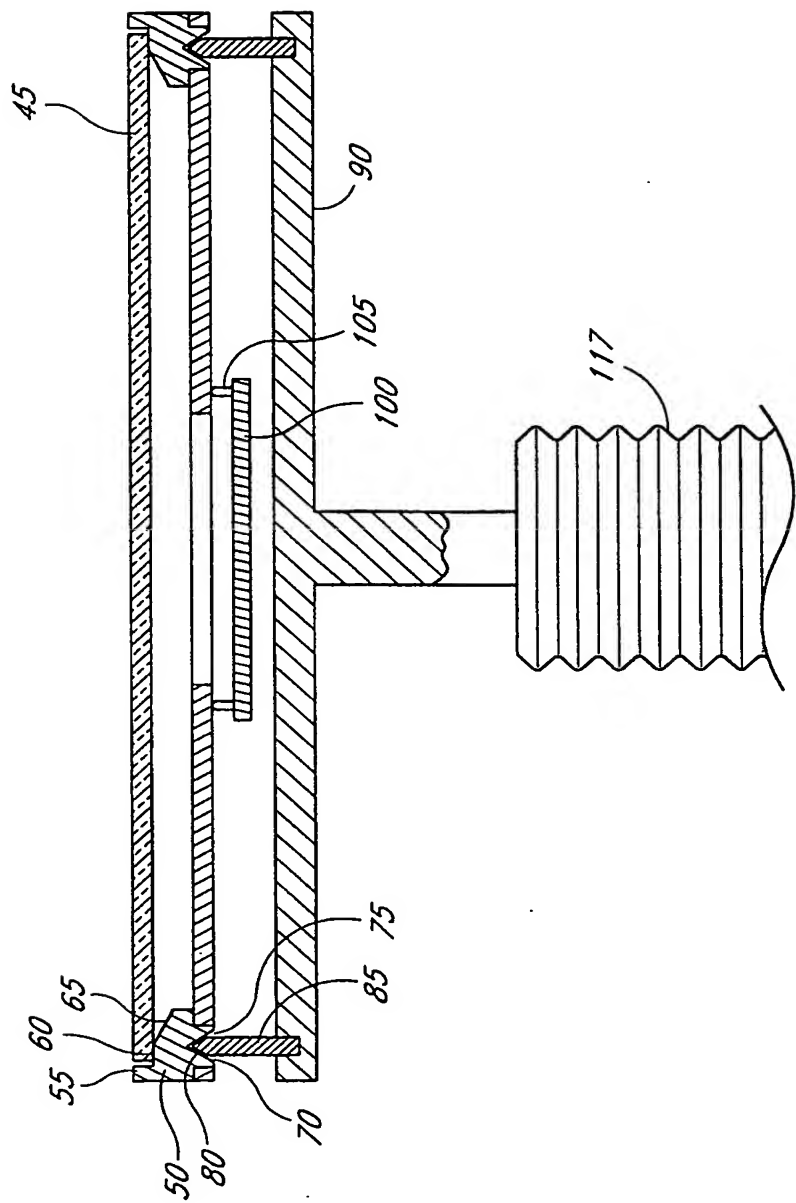


FIG. 5

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FIG. 6

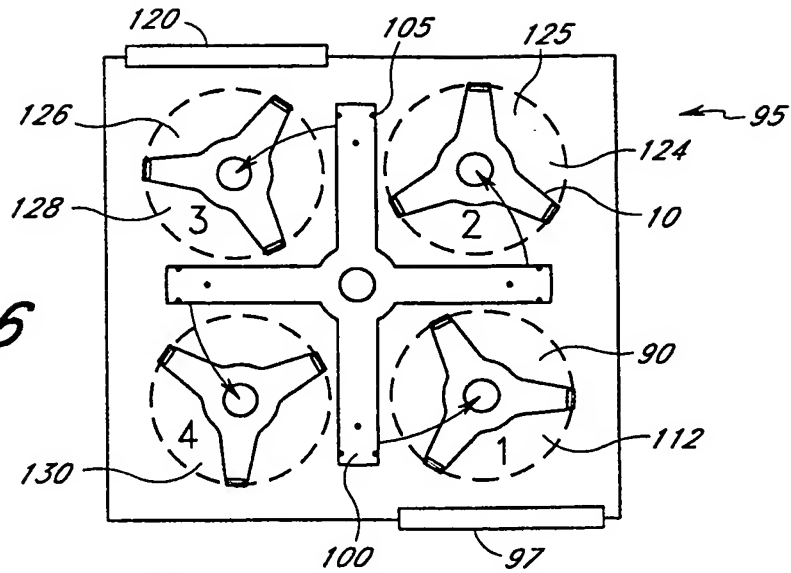


FIG. 7

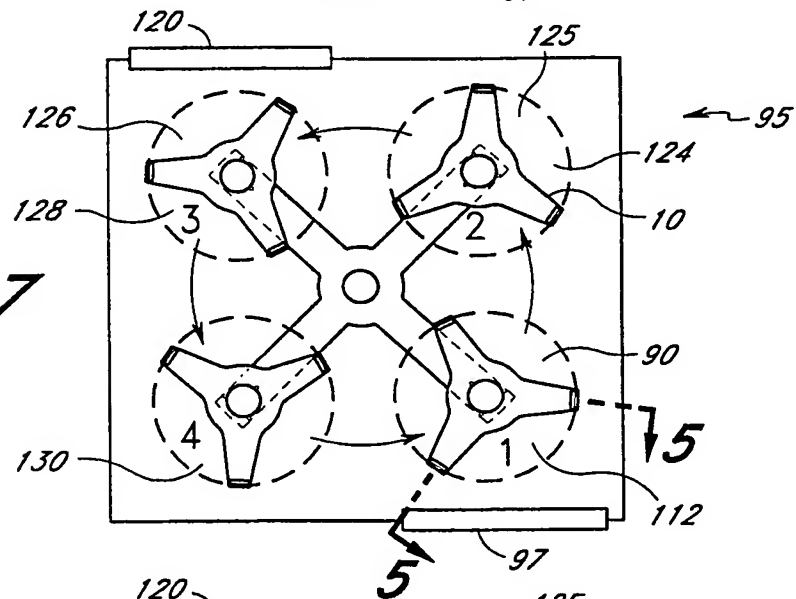
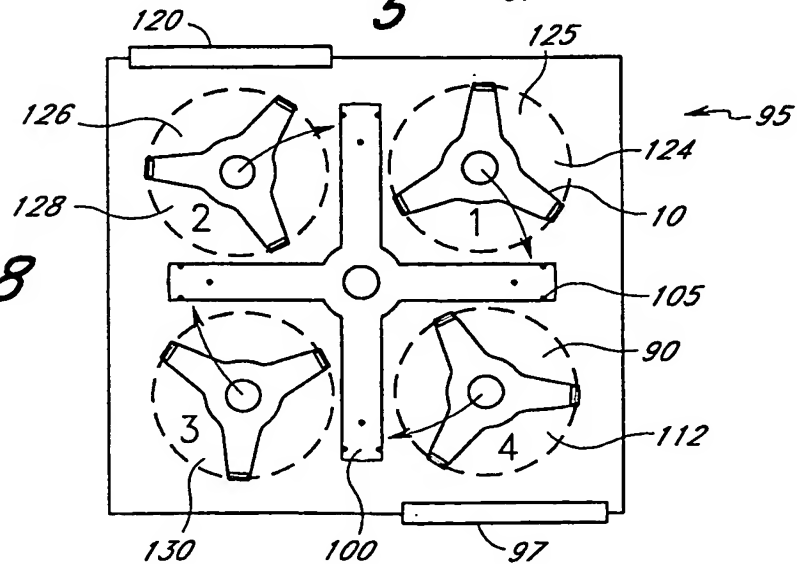


FIG. 8



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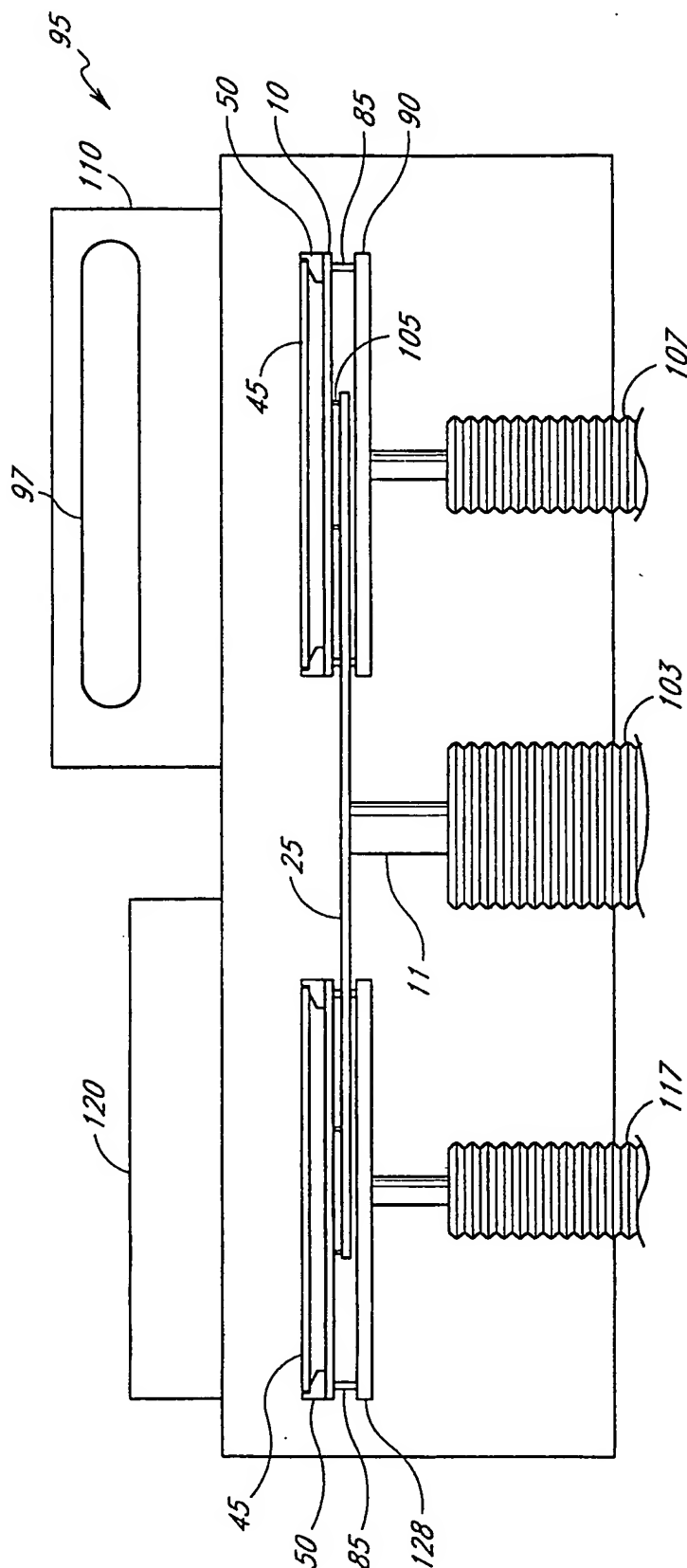


FIG. 9

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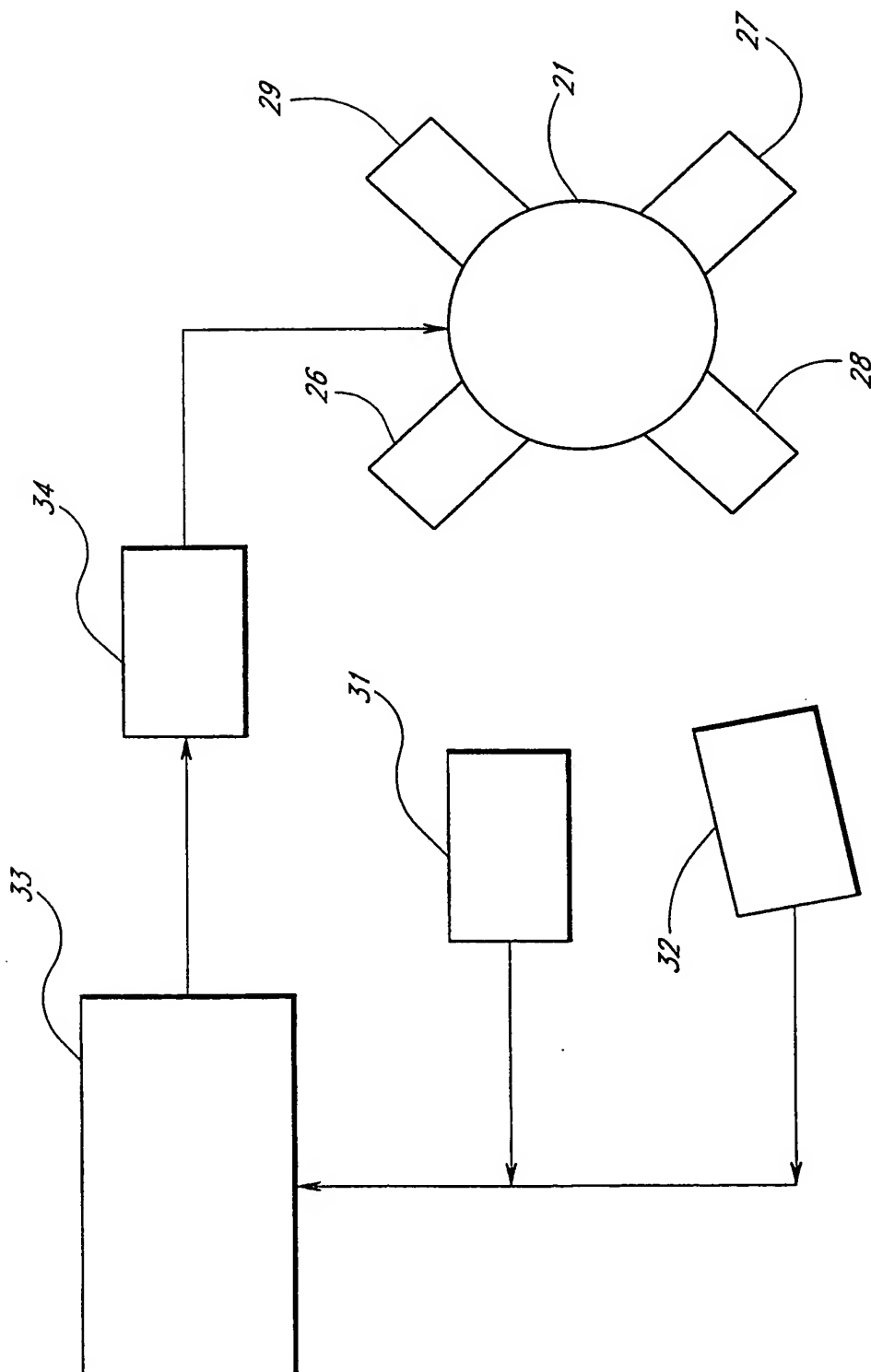


FIG. 9A

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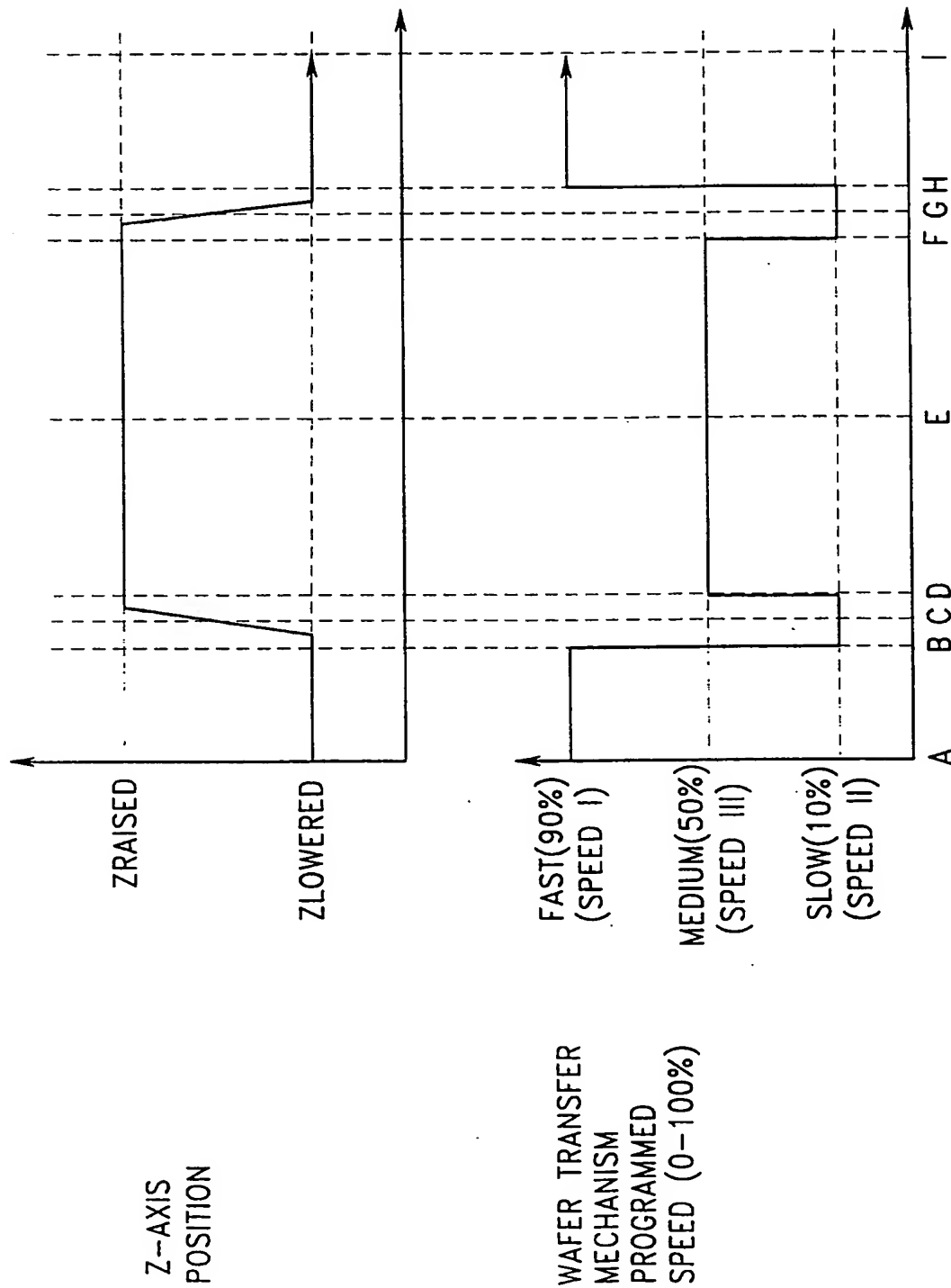


FIG. 10